

Shenzhen K&D Technology Co.,Ltd

		ECIFICATION FOR CD MODULE	
Customer			
Product M	lodel:	KD50G10-40M	NC-A3
Sample co	ode:		
Designed by		Checked by	Approved by

Final Approval by Customer

LCM Machinery OK	
Checked By	NG, Problem survey:
LCM Display OK	
Checked By	Approved By

* The specification of "TBD" should refer to the measured value of sample . If there is difference between the design specification and measured value, we naturally shall negotiate and agree to solution with customer.

Revision History

Version	Contents	Date	Note
A	Original	2009.12.15	

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1 Numbering System

			-			
(1)	(2)	(3) (4)	(5)	(6)	(7)	(8)

No	Definition	Specifications
(1)	TFT LCM Productor No.	KD Kingdisplay technologiy Co.,Ltd
(2)	Display monitor opposite angle line size	Unit :mm or mmm (size <10 inch: takes two integers ; size >=10 inch: takes three integers)
(3)	Productor Types	D Digital photo frame / DVD GGPS MMP PMobil-Phone
(4)	Productor Development Series No.	By two figures characters expression from 01 to 99
(5)	Interface PIN Number	By two figures characters expression from 01 to 99
(6)	With Touch Panel Or Not	TWith T/P ; NWithout T/P
(7)	LCD Type	AAUO ; MCMO ; CCPT; PPVI; LLG; WWintek; HHSD; TTopply; YHydis; IHitach; SSharp
(8)	Productor Development edition No.	By The English litters : A 1~ Z9
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2 Scope

This specification applies to the TFT LCD module which is designed and manufactured by LCM Factory of Shenzhen K&D Technology Co.,Ltd.

3 Normative Reference

GB/T4619-1996 《 Liquid Crystal Display Test Method》

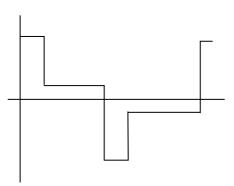
GB/T2424 《Basic environmental Testing Procedures for Electric and Electronic Products.》

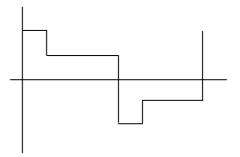
GB/T2423 《Basic Testing Procedures for Electric and Electronic Products》 IEC61747-1 《SIXTH PARTGB2828`2829-87《National Standard of PRC》

4 Definitions

4.1 Definitions of Vop

The definitions of threshold voltage Vth1, Vth2 the following typical waveforms are applied on liquid crystal by the method of equalized voltage for each duty and bias.





[selected waveform]

[non-selected waveform]

① Vth1: The voltage which the brightness of segment indicates 50% of saturated value on the conditions of selected waveform $(f_f=80Hz, \Phi=10^{\circ} \theta=270^{\circ} \text{ at } 25^{\circ}\text{C})$

2 Vth2: The voltage which the brightness of segment indicates 50% of saturated value on the conditions of non-selected waveform

(f_f=80Hz, Φ =10° θ =270° at 25°C)

③ Vop: (Vth1(50%)+Vth2(50%))/2 ($f_f=80Hz$, $\Phi=10^\circ \theta=270^\circ \text{ at } 25^\circ C$)

4.2 Definition of Response Time Tr, Td

(1)Tr: The time required which the brightness of segment becomes 10% from 100% when waveform is switched to selected one from non-selected one. ($f_f=80Hz$, $\Phi=10^{\circ}\theta=270^{\circ}at 25^{\circ}C$)

2)Td: The time required which the brightness of segment becomes 90% from 10% when waveform is switched to

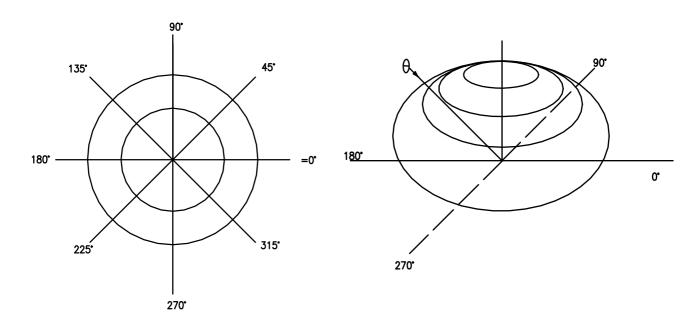
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selected one from selected one. (f_=80Hz, $\Phi {=}10^\circ\theta {=}270^\circ\!at$ 25 $^\circ\!\mathrm{C}$)

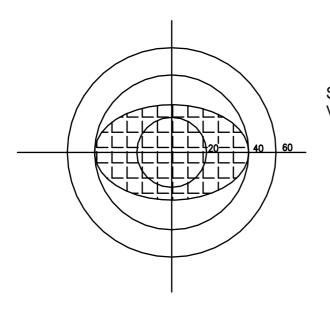
4.3 Definition of Contrast Ratio Cr

- Cr=A/B
 - ① A: Segments brightness in case of non-selected waveform
 - ② B: Segments brightness in case of selected waveform

4.4 Definition of Angle and Viewing Range



Angular Graph: Constrast Ratio



Such as: Viewing Angle Range: 80(Cr>2) Horizontal 70(Cr>2) Vertical

5 Technology Specifications

5.1 Feature

This single-display module is suitable for use in Multidedia Player products. The LCD adopts one backlight with High brightness 12-lamps white LED.

- 1) Construction: 5" a -Si color TFT-LCD , White LED backlight and FPC.
- 2) LCD:
 - 2.1 Amorphous-TFT 5-inch display, transmissive, normally white type.
 - 2.2 480(RGB) × 272 dots Matrix.
 - 2.3 Narrow-contact ledge technique.
 - 2.4 LCD Driver IC: NT39024CH-D/3EA \ ILI6480G
- 3) RGB interface.
- 4) Video signal interface: Parallel RGB.

5.2 Mechanical Specifications

Item	Specifications	Unit
Dimensional outline	120.70(W) ×75.80(H)×3.1 Max(T)	mm
Active area	110.88(W) ×62.83 (H)	mm
Pixel size	231(W) ×231(H)	um
Resolution	480(RGB) ×272	pixel

5.3 Absolute Max. Rating

ltem	Symbol		Value	Unit	Remark	
nem		Min	typ	Max	Onit	Remark
Logic power supply	DVDD	3.0	3.3	3.6	V	
Input high voltage	V _{IH}	0.7DVDD	-	DVDD	V	
Input low voltage	V _{IL}	0	-	0.3DVDD	V	
Operating temperature	T _{OPR}	-20		+70	°C	
Storage temperature	T_{STG}	-30		+80	°C	
Clock frequency	F _{CLK}	-	9	15	MHz	Note1

Note: For parallel RGB interface, maximum clock frequency is 15MHz,

5.4 Electrical Characteristics (VSS=0V,Ta=-20 to 70℃)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Input low leakage current	I _{IL}	No pull-up or pull-down	-1	-	1	μA
Input high leakage current	I _{IH}	No pull-up or pull-down	-1	-	1	μΑ
Output low voltage	V _{OL} 1	I _{oL} =1mA	DVSS	-	DVSS+0.4	V
Output high voltage	V _{OH} '1	I _{он} =-1mA	DVDD-0.4	-	DVDD	V
Gamma resistor	R _{GMA}	AVDD=5V, T _A =25⁰C, V0 to Vn=4V	-	17.1		kΩ
Driver output ourrent	I _{VOL}	AVDD=5V, V _o =0.2V, V _{IN} =0.7V	-	-	(80)	μA
Driver output current	I _{VOH}	AVDD=5V, V ₀ =4.8V, V _{IN} =4.3V	(80)	Ø	<u>- <</u>	μA
Average output deviation (chip to chip)	ΔV_{d}	Center gary level	S.	»-	10	mV
Output deviation	ΔV_{O1}	V _o =1.5V to AVDD-1.5V	SE D	(±10)	(±15)	mV
Output deviation	ΔV_{O2}	V _o < 1.5V or >AVDD-1.5V		(±20)	(±40)	mV
Logic dynamic current	I _{DD1}	DVDD	D = 0	(1.0) ^{*2}	(2.0)*3	mΑ
Driver dynamic current	I _{DD2}	AVDD		8.75	10 ^{*5}	mΑ
Input pull up/down resistance	Rı	V _{IL} = 0V or V _{IH} = DVDD	150	-	-	kΩ

Note:

- 1. CKV, STV, OEV pins
- 2. Parallel interface, f_{CLK}=9MHz, f_{STB}=17.14KHz, dot inversion pattern, DVDD=2.5V, TA=25°C
- 3. Parallel interface, f_{CLK}=9MHz, f_{STB}=17.14KHz, dot inversion pattern, DVDD=2.75V, TA=-30 to 85°C
- Parallel interface, f_{CLK}=9MHz, f_{STB}=17.14KHz, all white (NBW="H") pattern, AVDD=5V, V0=4.5V, Vn=0.2V, TA=25°C
- Parallel interface, f_{CLK}=9MHz, f_{STB}=17.14KHz, all white (NBW="H") pattern, AVDD=5.2V, V0=4.5V, Vn=0.2V, TA=-30 to 85°C

5.5 Optical specifications

Item	Item Symbol Conditions Specifications		Unit	Note				
item		Symbol	Conditions	Min. Typ. Max.		Onit	Note	
Transmittance		T%			7.5		%	
Contrast Ratio	0	CR		150	250			All left side data
Response Tin		TR			15	20	ms	are based on
response nin	ne	Tr			35	50	ms	CMO's following
	Red	X _R		0.585	0.615	0.645		condition -T6
	Neu	YR	Viewing normal angle	0.314	0.344	0.374		NTSC: 50%
	Green	X _G	$\theta_x = \theta_y = 0^\circ$	0.277	0.307	0.337		LC:5091
Chromaticity	Green	YG	0 _X = 0 _Y =0	0.532	0.562	0.592		Light : C light
Chromaticity	Blue	XB		0.103	0.133	0.163		(Machine:BM5A)
	Diue	YB		0.120	0.150	0.180		Normal Polarizer Without DBEF
	White	X_{W}		0.279	0.309	0.339		Simulation
	white	Yw		0.320	0.350	0.380		Data Reference
	Hor.	θ _{X+}			45			
Viewing	HOI.	θ _X .	Center		45			Only
Angle	Ver	θ _{Y+}	CR≥10		15		deg.	
	Ver.	θγ.			35			

*Note (1) Definition of Contrast Ratio (CR):

The contrast ratio can be calculated by the following expression.

Contrast Ratio (CR) = L63/L0

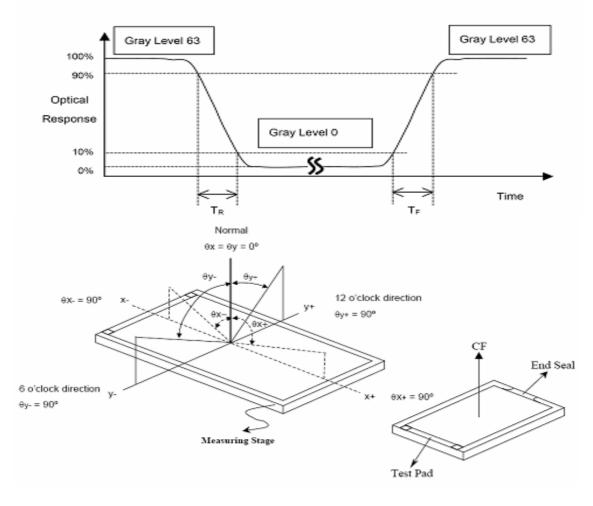
L63: Luminance of gray level 63

L0: Luminance of gray level 0

CR = CR(10)

CR (X) is corresponding to the Contrast Ratio of the point X at Figure in Note (5).

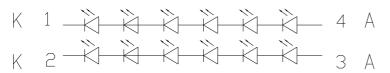
*Note (2) Definition of Response Time (TR, TF):



	5.6 LED back light sp	pecification (1	2 White Chips)
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Item	Symbol	Condition	Min	Тур	Max	Unit
Forward Voltage	Vf	lf=40mA	18	19.2	20.4	V
Uniformity (with L/G)	$\Delta \mathbf{B}_{\mathbf{p}}$	lf=40mA	80	-	-	%
Luminance for LCD	Lv	lf=40mA	4800	-	-	cd/m ²

LED CIRCUIT

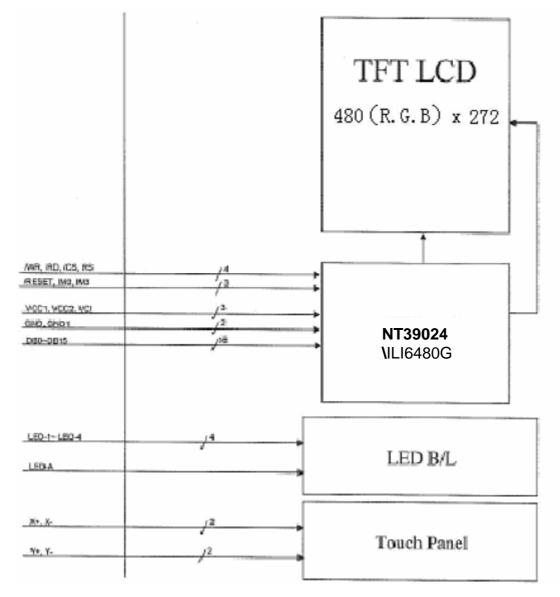


5.7 Interface Pin Connections

PIN NO.	Symbol	Description	
1	LED-K	LED backlight(Cathode)	
2	LED-A	LED backlight(anode)	
3	GND	Ground	
4	VDD	Power supply (Digital +3.0V)	
5-12	R0-R7	Red Data	
13-20	G0-G7	Green Data	
21-28	B0-B7	Blue Data	
29	GND	Ground	
30	CLK	Clodk	
31	DISP	Display on/off	
32	HSYNC	Horizontal sync input in RGB mode (short to GND if not used)	
33	VSYNC	Vertical sync input in RGB mode (short to GND if not used)	
34	DEN	Data Enable	
35	NC	NC	
36	GND	Ground	
37	NC	NC	
38	NC	NC	
39	NC	NC	
40	NC	NC	

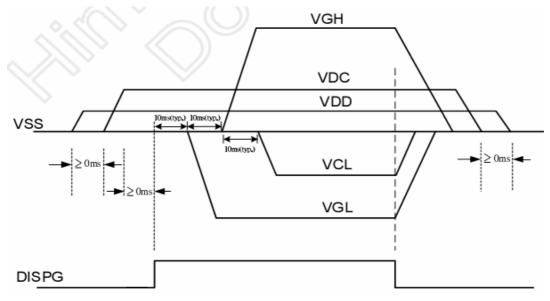
6 Signal timing diagram and Circuit block diagram

6.1 Circuit block diagram



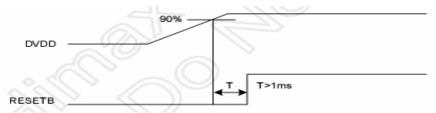
6.2 Signal Timing Diagram

6.2.1 Power ON/OFF Sequence



6.2.2 Reset timing

The reset input must be held for at least 1ms after power is stable.



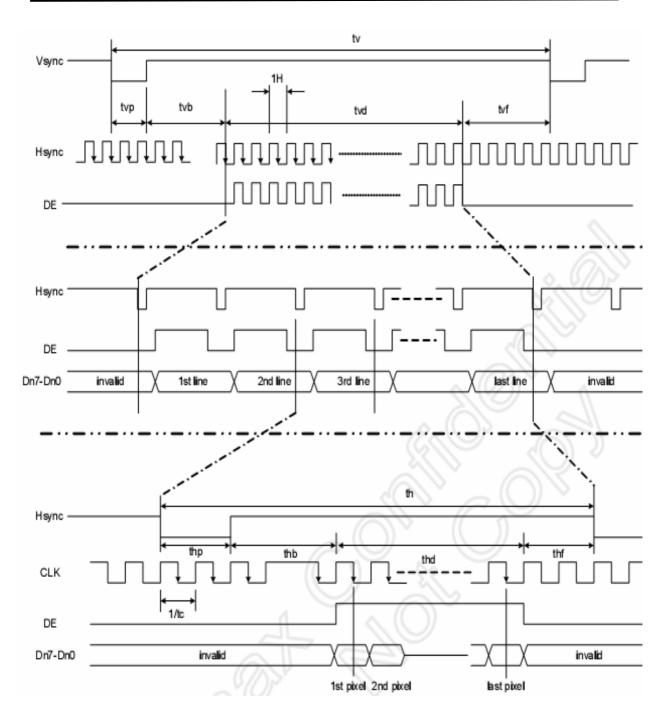
6.2.3 Timing Diagram of interface Signal

PARAMETER	Symbol	Min.	Тур.	Max.	Unit
Clock cycle	1/t _c *1	-	9	15	MHz
Hsync cycle	1/f _H	-	17.14	-	KHz
Vsync cycle	1/f _v	-	59.94	-	Hz
Horizontal Signal					
Horizontal cycle	th ¹²	-	525	-	CLK
Horizontal display period	thd	-	480	-	CLK
Horizontal front porch	thf	2	-	-	CLK
Horizontal pulse width	thp	2	41	-	CLK
Horizontal back porch	thb	2	2	-	CLK
Vertical Signal					10
Vertical cycle	tv	-	286	-	_~H
Vertical display period	tvd	-	272	(<u>с</u> н
Vertical front porch	tvf	1	2	-	Н
Vertical pulse width	tvp	1	10	. (7/0	$^{>}$ H
Vertical back porch	tvb	1	2		Н

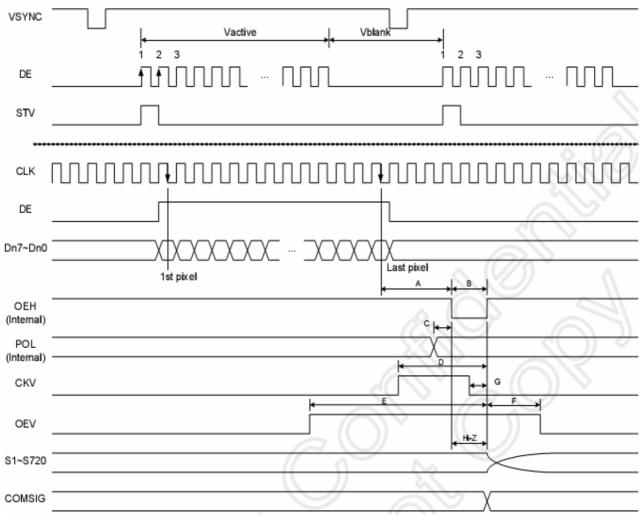
(480RGBx272, T_A=25°C, DVDD=2.25V to 3.6V, DVSS= 0V)

Note:

- 1. Parallel interface. Clock frequency and horizontal signal parameters are tripled in serial interface. The Maximum clock frequency of serial interface is 33MHz
- 2. thd=480CLK, thf=2CLK, thp=41CLK, thb=2CLK, thf + fhp + ftb > 44



6.2.4 Vertical Timing Diagram



Timing	480RGBx272	480RGBx240	240RGBx320	240RGBx240
A	8	8	4	4
В	4	4	2	2
С	0	0	0	0
D	10	10	5	5
E	34	34	17	17
F	6	6	3	3
G	2	2	1	1

7 Reliability Test Conditions And Methods

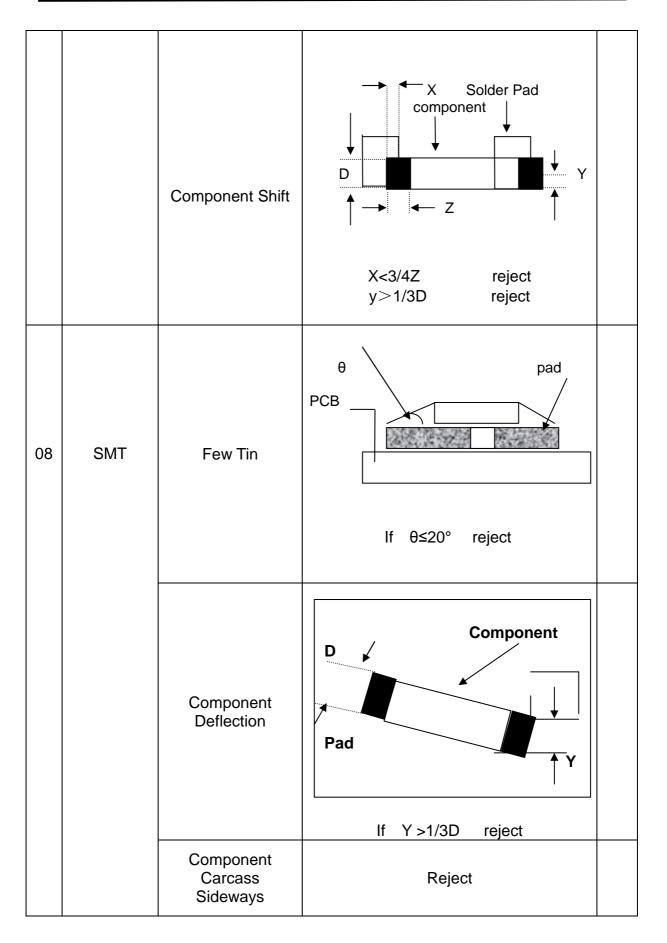
NO	Item	Condition	Method
1	High / Low Temperature Storage	80℃/-30℃ 120hrs	Check and record every 48Hrs
2	High / Low Temperature Life	70℃/-20℃ 120hrs (operating mode)	Check and record every 48Hrs
3	High Temperature、 High Humidity Operating	60℃,90% RH, 96Hrs	Check and record every 48hrs
4	Thermal Shock	-30℃(30Min) → 25℃(5Min) → 80℃(30Min) (conversion time, : 5 sec) 20 cycles	Each 10 cycles end , check
5	Vibration	10Hz~55Hz~10Hz Amplitude: 1.5mm 2hrs for each direction(X,Y,Z)	Each direction end, Check the Appearance and Electrical Characteristics
6	Static Electricity	Gap mood: ±1KV~±8KV (10 times air discharge with positive/negative voltage voltage gap : 1kv) Touch mood: ±1KV~±4KV	Each discharge end, Check the Electrical Characteristics
7	Curve	60 Thousand times, 40 times/min 150° (according to die if exist)	Check and record every 2~4 thousand times
8	Slump	Free faller movement for each side、cording、angle (75cm High、6 sides、2 angle、2 cording)	End

8 Inspection standard

No	Item	Criterion				
01	Outline Dimension	In accord with drawing				
02	Position-fin ding Dimension Assemble Dimension	In accord with drawing				
		Round type: non displa 3.1 Small area LCD	y Unit : mm			
			Dimension	Qualified Quantity		
		\rightarrow \times \leftarrow \uparrow	D≪0.1	Ignore		
			0.1 <d≤0.15< td=""><td>2</td></d≤0.15<>	2		
			D>0.15	0		
03	LCD black spots, white spots	3.2Large area LCD				
03	(Round type)		Dimension	Qualified Quantity		
		\rightarrow x \leftarrow \uparrow	D≤0.1	Ignore		
			0.1 <d≤0.15< td=""><td>2</td></d≤0.15<>	2		
			0.15 <d≤0.20< td=""><td>1</td></d≤0.20<>	1		
			D>0.20	0		
		C-STN : if D>0.1 , und	ualified			

		Unit : mm	4.1	Small	area	LCD
			Length	Width	Qualifi Quanti	
			-	≪0.015	Ignor	e
			≤1.0	0.015 <w≤< td=""><td>2</td><td></td></w≤<>	2	
			≤2.0	0.025	1	
			≤1.0	0.025 <w≤ 0.05</w≤ 	1	
	LCD black		-	D>0.05	Accordin circle	
04	spots, white spots		4.2Larç	ge area LCD		
	(Line Style)	(Line Style) \downarrow \downarrow \downarrow \downarrow \downarrow \downarrow \downarrow \downarrow	Length	Width	Qualifi Quanti	
			-	≪0.015	Ignor	е
			≹ 2.0	0.015 <w≤ 0.025</w≤ 	2	
			≤1.0	0.025 <w≪ 0.05</w≪ 	1	
			-	D>0.05	Accordin circle	
	LCD Scratch	Same to NO.3 o	circle	STN : If W ≥ 0.0 Ignore beyc		
05	Threadlike	sightline and surface of LCD is vertical (2)Same to NO.3 line style				
06	POL	It is not admissible that POL is beyond the edge of glass else, unqualified. It is essential that POL is over the 50 percent of width o frame, else, unqualified. According to the drawing in case of special definition.			-	
07	IC/FPC Bonding	Scratch		Reject		

		Intensity Of Adhesion	If lower than specification, reject
		Gold Fold Twist	Reject
07	IC/FPC	Silicon	According to outline, no gold outside, seal can not be higher than LCD
07	Bonding	FPC Gold Sever	Reject
08	SMT	Lack of Component、 Polarity Inverse	If exist, reject
		Leak Solder、 Virtual Solder	lf exist, reject
		Short Circuit In Solder Point	If exist, reject
		Tin Ball	If exist, reject
		Tin Acumination	If visual, reject
		Height Solder Point	If higher 0.5mm than component. reject
		Height of component	Either side higher 0.5mm than component, reject



		Component Carcass Sideways	If exist with visual inspection , reject	
	Lot Tin		 A: Tin accrete the solder side completely , hollowly ,Ok B: Tin accrete the solder side completely , full circle arc , ok C: Jointing include whole solder side, height of tin>50 percent of height of component, reject 	
		Few Tin A: Tin accrete the solder s completely , hollowly ,Ok B: height of tin > 1/3 of solder side component , ok C: height of tin ≤ 1/3 of solder side component, reject		
08	SMT	Normal Jointing side		
		Short circuit 、 Open circuit	Forbid	
09	Light	Quality of CSTN Display	 Rolling strake with visual inspection, forbid Differentness of color in viewing area with visual inspection (full white, red, green, blue), forbid Display change with visual inspection, forbid 	

			×	V		
		white	x ±0.05	y ±0.05		
		Red	±0.05	±0.05	Drive LCD under normal	
10	Color Of CIE	Green	±0.05	±0.05	condition, 25 °C Φ =0 Θ =0	
	Coordinate	Blue	±0.05	±0.05	Test white、red、green blue	
		According to the s			with DMS Record	
		or samp				
		approved				
11	Brightness	In accord with		specification Measure to 3, Adjust to burrow ag press "me display is s	ocation is in Follow Picture orightness instrument tozero , ainst the surface of LCD , easure", record when the	
	11 Brightness	prod specifie				
					Measure location	
12	CR (Max)	According to specification			ng to product specification re instrument (DMS-501)	
13	Response time	According to specification			ng to product specification re instrument (DMS-501)	
14	Viewing angle	According to specification			ng to product specification re instrument (DMS-501)	
15	Vibration、 Ring	Compare with the sample customer supply		I Compare with the sample clisioner.		
16	Frequency Of FPC Bend	Accordin use of p (main f foldawa phone thousa	FPC of ay cell ≥ 6		Measure instrument Bend angle : 150° °C in the casement when customer supply	

9 Handling Precautions

9.1 Mounting method

The LCD panel of Daxian LCD module consists of two thin glass plates with polarizes which easily be damaged. And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

9.2 Caution of LCD handling and cleaning

When cleaning the display surface, Use soft cloth with solvent [recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (Cl) , Salfur (S)

If goods were sent without being sili8con coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Salfur (S) from customer, Responsibility is on customer.

9.3 Caution against static charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to Vdd or Vss, do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

9.4 packing

- Module employ LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

9.5 Caution for operation

- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified

operation temperature.

- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the maximum operating temperature, 50%Rh or less is required.

9.6 storage

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it . And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
 [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

9.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water

10 Precaution for use

10.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

10.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to Daxian , and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

11 Dimensional Outline

